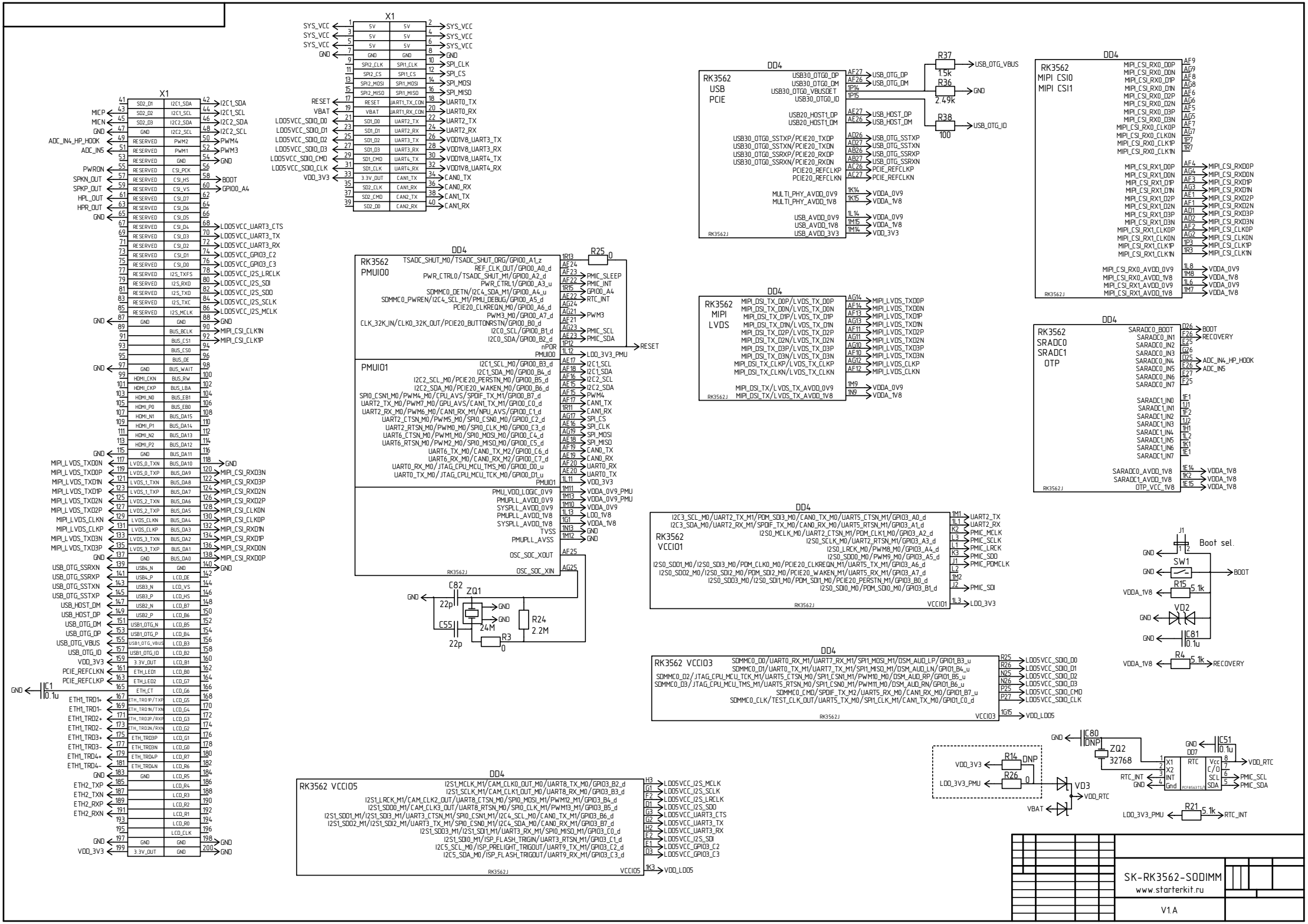
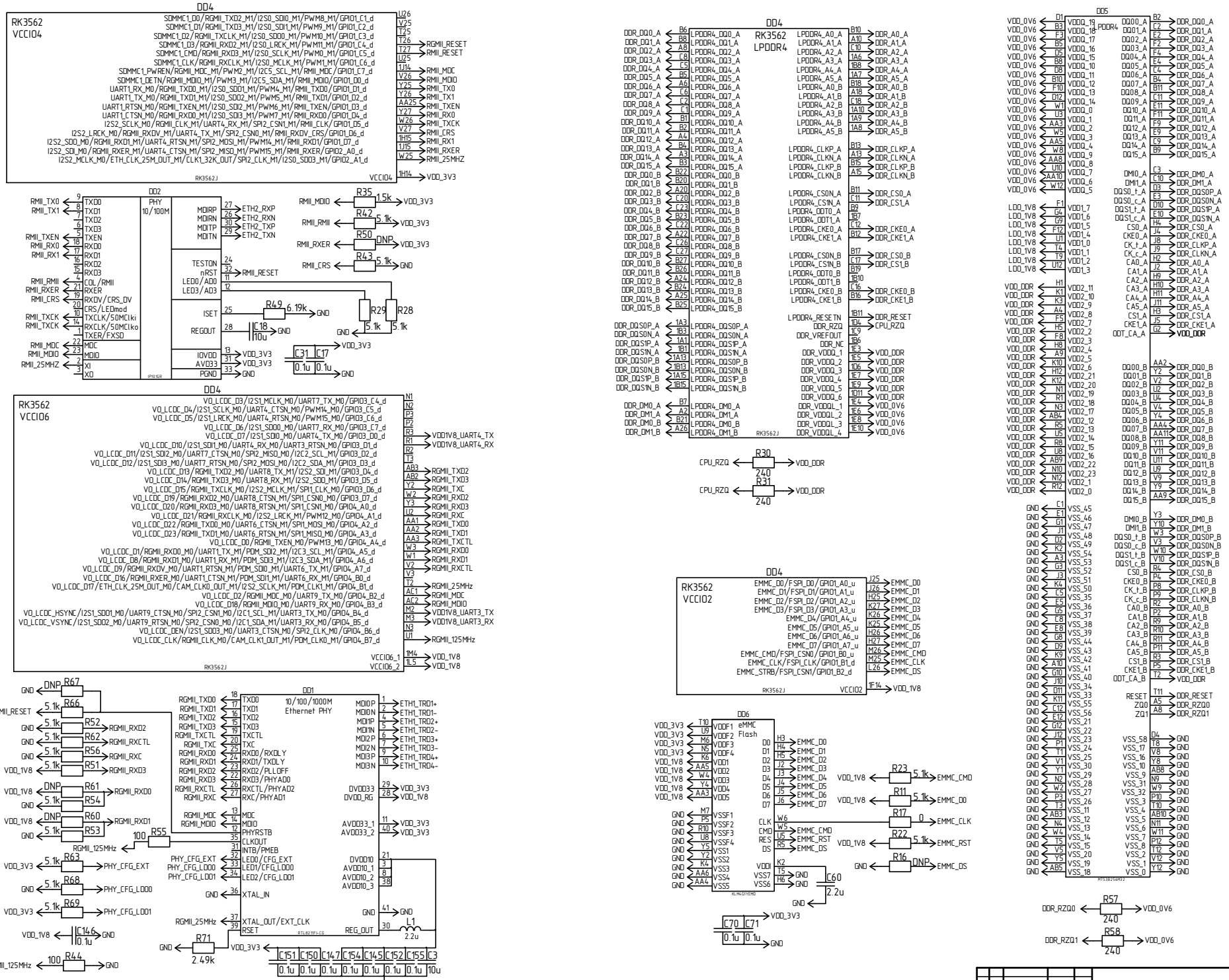

X1
DD4
RK3562 USB PCIE
RK3562 MIPI CSI0 MIPI CSI1
RK3562 PMUIO0
RK3562 MIPI LVDS
RK3562 SRADC0 SRADC1 OTP
RK3562 VCCIO1
RK3562 VCCIO3
RK3562 VCCIO5
R37 1.5k
R36 2.49k
R38 100
R25 0
R24 2.2M
R3 0
C82 22p
C55 22p
ZQ1 24M
C1 0.1u
J1 Boot sel.
SW1
R15 5.1k
VD2
C81 0.1u
R4 5.1k
C80 DNP
ZQ2 32768
C51 0.1u
DD7
PCF8563TS/8
R14 DNP
R26 0
VD3
R21 5.1k
SK-RK3562-SODIMM
www.starterkit.ru
V1.A


DD4
RK3562
VCCIO4
SDMMC1_D0/RGMII_TXD2_M1/I2S0_SDI0_M1/PWM8_M1/GPIO1_C1_d
SDMMC1_D1/RGMII_TXD3_M1/I2S0_SDI1_M1/PWM9_M1/GPIO1_C2_d
SDMMC1_D2/RGMII_TXCLK_M1/I2S0_SDO0_M1/PWM10_M1/GPIO1_C3_d
SDMMC1_D3/RGMII_RXD2_M1/I2S0_LRCK_M1/PWM11_M1/GPIO1_C4_d
SDMMC1_CMD/RGMII_RXD3_M1/I2S0_SCLK_M1/PWM0_M1/GPIO1_C5_d
SDMMC1_CLK/RGMII_RXCLK_M1/I2S0_MCLK_M1/PWM1_M1/GPIO1_C6_d
SDMMC1_PWREN/RGMII_MDC_M1/PWM2_M1/I2C5_SCL_M1/RMII_MDC/GPIO1_C7_d
SDMMC1_DETN/RGMII_MDIO_M1/PWM3_M1/I2C5_SDA_M1/RMII_MDIO/GPIO1_D0_d
UART1_RX_M0/RGMII_TXD0_M1/I2S0_SDO1_M1/PWM4_M1/RMII_TXD0/GPIO1_D1_d
UART1_TX_M0/RGMII_TXD1_M1/I2S0_SDO2_M1/PWM5_M1/RMII_TXD1/GPIO1_D2_d
UART1_RTSN_M0/RGMII_TXEN_M1/I2S0_SDI2_M1/PWM6_M1/RMII_TXEN/GPIO1_D3_d
UART1_CTSN_M0/RGMII_RXD0_M1/I2S0_SDI3_M1/PWM7_M1/RMII_RXD0/GPIO1_D4_d
I2S2_SCLK_M0/RGMII_CLK_M1/UART4_RX_M1/SPI2_CSN1_M1/RMII_CLK/GPIO1_D5_d
I2S2_LRCK_M0/RGMII_RXDV_M1/UART4_TX_M1/SPI2_CSN0_M1/RMII_RXDV_CRS/GPIO1_D6_d
I2S2_SDO_M0/RGMII_RXD1_M1/UART4_RTSN_M1/SPI2_MOSI_M1/PWM14_M1/RMII_RXD1/GPIO1_D7_d
I2S2_SDI_M0/RGMII_RXER_M1/UART4_CTSN_M1/SPI2_MISO_M1/PWM15_M1/RMII_RXER/GPIO2_A0_d
I2S2_MCLK_M0/ETH_CLK_25M_OUT_M1/CLK1_32K_OUT/SPI2_CLK_M1/I2S0_SDO3_M1/GPIO2_A1_d
RGMII_RESET
RMII_RESET
RMII_MDC
RMII_MDIO
RMII_TXD1
RMII_TXD0
RMII_TXEN
RMII_RXD1
RMII_RXD0
RMII_RXER
RMII_CRS
RMII_TXCK
RMII_RXCK
RMII_RXER
RMII_25MHZ
VDD_3V3
RK3562J
DD2
PHY
10/100M
TXD0
TXD1
TXD2
TXD3
TXEN
RXD0
RXD1
RXD2
RXD3
COL/RMII
RXER
RXDV/CRS_DV
CRS/LEDmod
TXCLK/50MClki
RXCLK/50MClko
TXER/FXSD
MDC
MDIO
XI
XO
MDIRP
MDIRN
MDITP
MDITN
TESTON
nRST
LED0/AD0
LED3/AD3
ISET
REGOUT
IOVDD
AVDD33
PGND
ETH2_RXP
ETH2_RXN
ETH2_TXP
ETH2_TXN
RMII_RESET
VDD_3V3
GND
R35 1.5k
R42 5.1k
R50 DNP
R43 5.1k
R49 6.19k
R29 5.1k
R28 5.1k
C18 10u
C31 0.1u
C17 0.1u
DD4
RK3562
VCCIO6
VO_LCDC_D3/I2S1_MCLK_M0/UART7_TX_M0/GPIO3_C4_d
VO_LCDC_D4/I2S1_SCLK_M0/UART4_CTSN_M0/PWM14_M0/GPIO3_C5_d
VO_LCDC_D5/I2S1_LRCK_M0/UART4_RTSN_M0/PWM15_M0/GPIO3_C6_d
VO_LCDC_D6/I2S1_SDO0_M0/UART7_RX_M0/GPIO3_C7_d
VO_LCDC_D7/I2S1_SDI0_M0/UART4_TX_M0/GPIO3_D0_d
VO_LCDC_D10/I2S1_SDI1_M0/UART4_RX_M0/UART3_RTSN_M0/GPIO3_D1_d
VO_LCDC_D11/I2S1_SDI2_M0/UART7_CTSN_M0/SPI2_MISO_M0/I2C2_SCL_M1/GPIO3_D2_d
VO_LCDC_D12/I2S1_SDI3_M0/UART7_RTSN_M0/SPI2_MOSI_M0/I2C2_SDA_M1/GPIO3_D3_d
VO_LCDC_D13/RGMII_TXD2_M0/UART8_TX_M1/I2S2_SDI_M1/GPIO3_D4_d
VO_LCDC_D14/RGMII_TXD3_M0/UART8_RX_M1/I2S2_SDO_M1/GPIO3_D5_d
VO_LCDC_D15/RGMII_TXCLK_M0/I2S2_MCLK_M1/SPI1_CLK_M0/GPIO3_D6_d
VO_LCDC_D19/RGMII_RXD2_M0/UART8_CTSN_M1/SPI1_CSN0_M0/GPIO3_D7_d
VO_LCDC_D20/RGMII_RXD3_M0/UART8_RTSN_M1/SPI1_CSN1_M0/GPIO4_A0_d
VO_LCDC_D21/RGMII_RXCLK_M0/I2S2_LRCK_M1/PWM12_M0/GPIO4_A1_d
VO_LCDC_D22/RGMII_TXD0_M0/UART6_CTSN_M1/SPI1_MOSI_M0/GPIO4_A2_d
VO_LCDC_D23/RGMII_TXD1_M0/UART6_RTSN_M1/SPI1_MISO_M0/GPIO4_A3_d
VO_LCDC_D0/RGMII_TXEN_M0/PWM13_M0/GPIO4_A4_d
VO_LCDC_D1/RGMII_RXD0_M0/UART1_TX_M1/PDM_SDI2_M1/I2C3_SCL_M1/GPIO4_A5_d
VO_LCDC_D8/RGMII_RXD1_M0/UART1_RX_M1/PDM_SDI3_M1/I2C3_SDA_M1/GPIO4_A6_d
VO_LCDC_D9/RGMII_RXDV_M0/UART1_RTSN_M1/PDM_SDI0_M1/UART6_TX_M1/GPIO4_A7_d
VO_LCDC_D16/RGMII_RXER_M0/UART1_CTSN_M1/PDM_SDI1_M1/UART6_RX_M1/GPIO4_B0_d
VO_LCDC_D17/ETH_CLK_25M_OUT_M0/CAM_CLK0_OUT_M1/I2S2_SCLK_M1/PDM_CLK1_M1/GPIO4_B1_d
VO_LCDC_D2/RGMII_MDC_M0/UART9_TX_M0/GPIO4_B2_d
VO_LCDC_D18/RGMII_MDIO_M0/UART9_RX_M0/GPIO4_B3_d
VO_LCDC_HSYNC/I2S1_SDO1_M0/UART9_CTSN_M0/SPI2_CSN1_M0/I2C1_SCL_M1/UART3_TX_M0/GPIO4_B4_d
VO_LCDC_VSYNC/I2S1_SDO2_M0/UART9_RTSN_M0/SPI2_CSN0_M0/I2C1_SDA_M1/UART3_RX_M0/GPIO4_B5_d
VO_LCDC_DEN/I2S1_SDO3_M0/UART3_CTSN_M0/SPI2_CLK_M0/GPIO4_B6_d
VO_LCDC_CLK/RGMII_CLK_M0/CAM_CLK1_OUT_M1/PDM_CLK0_M1/GPIO4_B7_d
VDD1V8_UART4_TX
VDD1V8_UART4_RX
RGMII_TXD2
RGMII_TXD3
RGMII_TXC
RGMII_RXD2
RGMII_RXD3
RGMII_RXC
RGMII_TXD0
RGMII_TXD1
RGMII_TXCTL
RGMII_RXD0
RGMII_RXD1
RGMII_RXCTL
RGMII_25MHz
RGMII_MDC
RGMII_MDIO
VDD1V8_UART3_TX
VDD1V8_UART3_RX
RGMII_125MHz
VCCIO6_1
VCCIO6_2
VDD_1V8
RK3562J
DD1
10/100/1000M
Ethernet PHY
TXD0
TXD1
TXD2
TXD3
TXCTL
TXC
RXD0/RXDLY
RXD1/TXDLY
RXD2/PLLOFF
RXD3/PHYAD0
RXCTL/PHYAD2
RXC/PHYAD1
MDC
MDIO
PHYRSTB
CLKOUT
INTB/PMEB
LED0/CFG_EXT
LED1/CFG_LDO0
LED2/CFG_LDO1
XTAL_IN
XTAL_OUT/EXT_CLK
RSET
MDI0P
MDI0N
MDI1P
MDI1N
MDI2P
MDI2N
MDI3P
MDI3N
DVDD33
DVDD_RG
AVDD33_1
AVDD33_2
DVDD10
AVDD10_1
AVDD10_2
AVDD10_3
REG_OUT
ETH1_TRD1+
ETH1_TRD1-
ETH1_TRD2+
ETH1_TRD2-
ETH1_TRD3+
ETH1_TRD3-
ETH1_TRD4+
ETH1_TRD4-
VDD_3V3
VDD_1V8
RTL8211F(I-)CG
DNP R67
R66 5.1k
R52 5.1k
R62 5.1k
R56 5.1k
R51 5.1k
DNP R61
R54 5.1k
DNP R60
R53 5.1k
R55 100
R63 5.1k
R68 5.1k
R69 5.1k
C146 0.1u
R44 100
R71 2.49k
L1 2.2u
C151 C150 C147 C154 C145 C152 C155 C3
0.1u 0.1u 0.1u 0.1u 0.1u 0.1u 0.1u 10u
PHY_CFG_EXT
PHY_CFG_LDO0
PHY_CFG_LDO1
DD4
RK3562
LPDDR4
LPDDR4_DQ0_A
LPDDR4_DQ1_A
LPDDR4_DQ2_A
LPDDR4_DQ3_A
LPDDR4_DQ4_A
LPDDR4_DQ5_A
LPDDR4_DQ6_A
LPDDR4_DQ7_A
LPDDR4_DQ8_A
LPDDR4_DQ9_A
LPDDR4_DQ10_A
LPDDR4_DQ11_A
LPDDR4_DQ12_A
LPDDR4_DQ13_A
LPDDR4_DQ14_A
LPDDR4_DQ15_A
LPDDR4_DQ0_B
LPDDR4_DQ1_B
LPDDR4_DQ2_B
LPDDR4_DQ3_B
LPDDR4_DQ4_B
LPDDR4_DQ5_B
LPDDR4_DQ6_B
LPDDR4_DQ7_B
LPDDR4_DQ8_B
LPDDR4_DQ9_B
LPDDR4_DQ10_B
LPDDR4_DQ11_B
LPDDR4_DQ12_B
LPDDR4_DQ13_B
LPDDR4_DQ14_B
LPDDR4_DQ15_B
LPDDR4_DQS0P_A
LPDDR4_DQS0N_A
LPDDR4_DQS1P_A
LPDDR4_DQS1N_A
LPDDR4_DQS0P_B
LPDDR4_DQS0N_B
LPDDR4_DQS1P_B
LPDDR4_DQS1N_B
LPDDR4_DM0_A
LPDDR4_DM1_A
LPDDR4_DM0_B
LPDDR4_DM1_B
LPDDR4_A0_A
LPDDR4_A1_A
LPDDR4_A2_A
LPDDR4_A3_A
LPDDR4_A4_A
LPDDR4_A5_A
LPDDR4_A0_B
LPDDR4_A1_B
LPDDR4_A2_B
LPDDR4_A3_B
LPDDR4_A4_B
LPDDR4_A5_B
LPDDR4_CLKP_A
LPDDR4_CLKN_A
LPDDR4_CLKP_B
LPDDR4_CLKN_B
LPDDR4_CS0N_A
LPDDR4_CS1N_A
LPDDR4_ODT0_A
LPDDR4_ODT1_A
LPDDR4_CKE0_A
LPDDR4_CKE1_A
LPDDR4_CS0N_B
LPDDR4_CS1N_B
LPDDR4_ODT0_B
LPDDR4_ODT1_B
LPDDR4_CKE0_B
LPDDR4_CKE1_B
LPDDR4_RESETN
DDR_RZQ
DDR_VREFOUT
DDR_NC
DDR_VDDQ_1
DDR_VDDQ_2
DDR_VDDQ_3
DDR_VDDQ_4
DDR_VDDQ_5
DDR_VDDQ_6
DDR_VDDQL_1
DDR_VDDQL_2
DDR_VDDQL_3
DDR_VDDQL_4
DDR_A0_A
DDR_A1_A
DDR_A2_A
DDR_A3_A
DDR_A4_A
DDR_A5_A
DDR_A0_B
DDR_A1_B
DDR_A2_B
DDR_A3_B
DDR_A4_B
DDR_A5_B
DDR_CLKP_A
DDR_CLKN_A
DDR_CLKP_B
DDR_CLKN_B
DDR_CS0_A
DDR_CS1_A
DDR_CKE0_A
DDR_CKE1_A
DDR_CS0_B
DDR_CS1_B
DDR_CKE0_B
DDR_CKE1_B
DDR_RESET
CPU_RZQ
VDD_0V6
RK3562J
R30 240
R31 240
DD4
RK3562
VCCIO2
EMMC_D0/FSPI_D0/GPIO1_A0_u
EMMC_D1/FSPI_D1/GPIO1_A1_u
EMMC_D2/FSPI_D2/GPIO1_A2_u
EMMC_D3/FSPI_D3/GPIO1_A3_u
EMMC_D4/GPIO1_A4_u
EMMC_D5/GPIO1_A5_u
EMMC_D6/GPIO1_A6_u
EMMC_D7/GPIO1_A7_u
EMMC_CMD/FSPI_CSN0/GPIO1_B0_u
EMMC_CLK/FSPI_CLK/GPIO1_B1_d
EMMC_STRB/FSPI_CSN1/GPIO1_B2_d
EMMC_D0
EMMC_D1
EMMC_D2
EMMC_D3
EMMC_D4
EMMC_D5
EMMC_D6
EMMC_D7
EMMC_CMD
EMMC_CLK
EMMC_DS
VDD_1V8
RK3562J
DD6
eMMC
Flash
VDDF1
VDDF2
VDDF3
VDDF4
VDD1
VDD2
VDD3
VDD4
VDD5
VSSF1
VSSF2
VSSF3
VSSF4
VSS1
VSS2
VSS3
VSS4
VSS5
D0
D1
D2
D3
D4
D5
D6
D7
CLK
CMD
RES
DS
VDD
VSS7
VSS6
EMMC_D0
EMMC_D1
EMMC_D2
EMMC_D3
EMMC_D4
EMMC_D5
EMMC_D6
EMMC_D7
EMMC_CMD
EMMC_RST
EMMC_DS
R23 5.1k
R11 5.1k
R17 0
R22 5.1k
R16 DNP
C60 2.2u
C70 C71
0.1u 0.1u
DD5
PDDR4
VDDQ_19
VDDQ_18
VDDQ_17
VDDQ_16
VDDQ_15
VDDQ_10
VDDQ_11
VDDQ_12
VDDQ_13
VDDQ_14
VDDQ_0
VDDQ_1
VDDQ_2
VDDQ_3
VDDQ_4
VDDQ_9
VDDQ_8
VDDQ_7
VDDQ_6
VDDQ_5
VDD1_7
VDD1_6
VDD1_5
VDD1_4
VDD1_0
VDD1_1
VDD1_2
VDD1_3
VDD2_11
VDD2_10
VDD2_9
VDD2_8
VDD2_7
VDD2_2
VDD2_3
VDD2_4
VDD2_5
VDD2_6
VDD2_21
VDD2_20
VDD2_19
VDD2_18
VDD2_17
VDD2_12
VDD2_13
VDD2_14
VDD2_15
VDD2_16
VDD2_22
VDD2_23
VDD2_1
VDD2_0
VSS_45
VSS_46
VSS_47
VSS_48
VSS_49
VSS_54
VSS_53
VSS_52
VSS_51
VSS_50
VSS_35
VSS_36
VSS_37
VSS_38
VSS_39
VSS_44
VSS_43
VSS_42
VSS_41
VSS_40
VSS_34
VSS_33
VSS_55
VSS_56
VSS_21
VSS_22
VSS_23
VSS_24
VSS_25
VSS_30
VSS_29
VSS_28
VSS_27
VSS_26
VSS_11
VSS_12
VSS_13
VSS_14
VSS_15
VSS_20
VSS_19
VSS_18
DQ00_A
DQ01_A
DQ02_A
DQ03_A
DQ04_A
DQ05_A
DQ06_A
DQ07_A
DQ08_A
DQ09_A
DQ10_A
DQ11_A
DQ12_A
DQ13_A
DQ14_A
DQ15_A
DM0_A
DM1_A
DQS0_t_A
DQS0_c_A
DQS1_t_A
DQS1_c_A
CS0_A
CKE0_A
CK_t_A
CK_c_A
CA0_A
CA1_A
CA2_A
CA3_A
CA4_A
CA5_A
CS1_A
CKE1_A
ODT_CA_A
DQ00_B
DQ01_B
DQ02_B
DQ03_B
DQ04_B
DQ05_B
DQ06_B
DQ07_B
DQ08_B
DQ09_B
DQ10_B
DQ11_B
DQ12_B
DQ13_B
DQ14_B
DQ15_B
DM0_B
DM1_B
DQS0_t_B
DQS0_c_B
DQS1_t_B
DQS1_c_B
CS0_B
CKE0_B
CK_t_B
CK_c_B
CA0_B
CA1_B
CA2_B
CA3_B
CA4_B
CA5_B
CS1_B
CKE1_B
ODT_CA_B
RESET
ZQ0
ZQ1
VDD_DDR
GND
MT53B256M32
R57 240
R58 240
DDR_RZQ0
DDR_RZQ1
VDD_0V6