



DD2
ARM7

- P0[0]RD1/TX/D3/SDA1
- P0[1]TD1/RXD3/SC1
- P0[2]TXD1
- P0[3]RXD1
- P0[4]I2SRX_CLK/L[0]/RD2/CAP[0]
- P0[5]I2SRX_WS/L[1]/T/D2/CAP[0]
- P0[6]I2SRX_SDA/L[8]/SSEL1/MAT[0]
- P0[7]I2STX_CLK/L[9]/SCK1/MAT[0]
- P0[8]I2STX_WS/L[6]/MOS1/MAT[0]
- P0[9]I2STX_SDA/L[17]/MOS1/MAT[0]
- P0[10]TXD2/SDA2/MAT[3]
- P0[11]RXD2/SC/L2/MAT[3]
- P0[12]USB_PPWR2/MOS1/AD0
- P0[13]USB_UP_LED2/CONNECT2/SS
- P0[14]USB_HSTEN2/USB_CONNECT2/SS
- P0[15]TXD1/SCK0/SC[0]
- P0[16]RXD1/SSEL0/SS[0]
- P0[17]CTS1/MISO/MOS[0]
- P0[18]DCD1/MOS0/MOS[0]
- P0[19]DSR1/MCICMD/SC[1]
- P0[20]DTR1/MCICMD/SC[1]
- P0[21]R1/MCIPWR/AD[6]
- P0[22]RTS1/MCIDAT0/T[0]
- P0[23]AD0[0]I2SRX_CLK/CAP[8]
- P0[24]AD0[1]I2SRX_WS/CAP[8]
- P0[25]AD0[2]I2STX_CLK/CAP[8]
- P0[26]AD0[3]AOUT/RX[0]
- P0[27]SDA2
- P0[28]SCL2
- P0[29]USB_DP
- P0[30]USB_DM
- P0[31]USB_P
- P0[32]USB_N
- P1[0]ENET_TXD0
- P1[1]ENET_TXD1
- P1[2]ENET_TXD2/MCICLK/PWM[0]
- P1[3]ENET_TXD3/CMD/PWM[0]
- P1[4]ENET_TX_EN
- P1[5]ENET_TX_ER/MCIPWR/PWM[0]
- P1[6]ENET_TX_CLK/MCIDAT0/PWM[0]
- P1[7]ENET_COL/MCIDAT1/PWM[0]
- P1[8]ENET_CRS_DV/ENET_CRS
- P1[9]ENET_RXD0
- P1[10]ENET_RXD1
- P1[11]ENET_RXD2/MCIDAT2/PWM[0]
- P1[12]ENET_RXD3/MCIDAT3/PCAP[0]
- P1[13]ENET_RX_D0
- P1[14]ENET_RX_D1
- P1[15]ENET_REF_CLK/ENET_MDIO
- P1[16]ENET_MDIO
- P1[17]ENET_MDIO
- P1[18]USB_UP_LED1/PWM1[1]/CAP[0]
- P1[19]USB_TX_E1/USB_PPWR1/CAP[0]
- P1[20]USB_TX_DP/L[6]/L[10]/PWM1[3]
- P1[21]USB_TX_DM/L[7]/L[11]/PWM1[3]
- P1[22]USB_RCV1/L[8]/L[12]/USB_PWRD1/MAT[0]
- P1[23]USB_RX_DP1/L[9]/L[13]/PWM1[4]
- P1[24]USB_RX_DM1/L[10]/L[14]/PWM1[5]
- P1[25]USB_LS1/L[11]/L[15]USB_HSTEN1/MAT[0]
- P1[26]USB_SS/PND1/L[12]/L[20]/PWM1[6]/CAP[0]
- P1[27]USB_INT1/L[13]/L[21]USB_OVRCR2/L[10]
- P1[28]USB_SCL1/L[14]/L[22]/PCAP1[0]/MAT[0]
- P1[29]USB_SDA1/L[15]/L[23]/PCAP1[1]/MAT[0]
- P1[30]USB_PPWR2/VBUS/AD0[0]
- P1[31]USB_OVRCR2/SCK1/AD0[0]
- P2[0]PWM1[1]/TXD1/TRACECLK/CDP[0]
- P2[1]PWM1[2]/RXD1/PIPESTAT0/CLD[0]
- P2[2]PWM1[3]/CTS1/PIPESTAT1/CLD[0]
- P2[3]PWM1[4]/DCD1/PIPESTAT2/CLD[0]
- P2[4]PWM1[5]/DSR1/RACESYNCL/CDENAL/CLD[0]
- P2[5]PWM1[6]/DTR1/TRACEPKT0/CLD[0]
- P2[6]PCAP1[0]/R1/TRACEPKT1[0]
- P2[7]RD2/RTS1/TRACEPKT2[1]
- P2[8]T/D2/TX/D2/TRACEPKT3[2]
- P2[9]USB_CONNECT1/RXD2/EXTIN0/L[3]
- P2[10]EINT1/INT0/CDCLKIN/MCIDAT1/2/STX
- P2[11]EINT2/L[4]/L[13]/L[18]/L[18]MCIDAT1/2/STX
- P2[12]EINT3/L[5]/L[9]/L[19]/MCIDAT3/2/STX
- P2[14]CS2/CAP2[0]/SD[0]
- P2[15]CS3/CAP2[1]/SC[0]
- P2[16]CA
- P2[17]RA
- P2[18]CLKOUT
- P2[19]CLKOUT
- P2[20]DYCS2
- P2[21]DYCS2
- P2[22]DYCS2/CAP3[0]/SC[0]
- P2[23]DYCS3/CAP3[0]/SC[0]
- P2[24]CKEOUT
- P2[25]CKEOUT
- P2[26]CKEOUT2/MAT3[0]/MOS[0]
- P2[27]CKEOUT3/MAT3[1]/MOS[0]
- P2[28]DQMOUT1
- P2[29]DQMOUT1
- P2[30]DQMOUT2/MAT3[2]/SD[0]
- P2[31]DQMOUT3/MAT3[3]/SC[0]

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V3.A