

CPU
ARM926EJ-S
400MHz

Memory
DDR2
64MBytes

Flash memory
NAND
256MBytes

Ethernet PHY
100/10M

uSD
(Bootable MCI0)

Power system

External
IO Interfaces:
2 x USB
Ethernet RX/TX
SD/MMC (MCI1)
LCD
TSADC
ISI
AC97
4 x UART
2 x SPI
I2C
EBI1 8 bit databus + 2 bit address +
CS + OE + WE

X5	
←	Name
GND←	1
USB_HIP←	2 USB1+
USB_HIM←	3 USB1-
GND←	4
USB_HM←	5 USB0-
USB_HP←	6 USB0+

X1			
GND←	1	PD12	2 → CPU_PD12
LCD_G2←	3	PE17	4 → LCD_R7
LCD_B2←	5	PE25	6 → LCD_B4
LCD_B6←	7	PE29	8 → LCD_B3
LCD_B7←	9	PE30	10 → LCD_B5
LCD_G7←	11	PE22	12 → LCD_G6
LCD_G5←	13	PE20	14 → LCD_B0
LCD_G4←	15	PE19	16 → LCD_G0
LCD_G1←	17	PE16	18 → LCD_R0
LCD_G3←	19	PE18	20 → LCD_B1
LCD_R6←	21	PE13	22 → LCD_R5
LCD_R3←	23	PE10	24 → LCD_R2
LCD_DE←	25	PE6	26 → LCD_DTCK
LCD_R1←	27	PE8	28 → LCD_VS
LCD_R4←	29	PE11	30 → LCD_HS
CPU_PC10←	31	PC10	32 → CPU_PD25
CPU_PC11←	33	PC11	34 → CPU_PC7
CPU_PC0←	35	PC0	36 → CPU_PC2
TS3←	37	PD22	38 → TS4
TS1←	39	PD20	40 → TS2

X3			
GND←	1		2 → 3.3V
UART2_TX←	3	PB6	4 → DRXD
UART0_RX←	5	PB18	6 → DTXD
UART0_TX←	7	PB19	8 → AC97_SDI
AC97_BCLK←	9	PB9	10 → AC97_SYNC
AC97_SDO←	11	PD7	12 → ISL_D3
ISL_D5←	15	PB25	16 → ISL_D4
ISL_D1←	17	PB21	18 → ISL_VS
ISI_D10←	19	PB8	20 → RESET
BATTERY←	21	SHDN	22 → WKUP
SHDN←	23	SHDN	24 → EBI1_NWE
EBI1_NRD←	25	EBI1_NRD	26 → EBI1_NCS0
EBI1_A14←	27	EBI1_A14	28 → EBI1_A15
EBI1_D1←	29	EBI1_D1	30 → EBI1_D0
EBI1_D3←	31	EBI1_D3	32 → EBI1_D2
EBI1_D5←	33	EBI1_D5	34 → EBI1_D4
EBI1_D7←	35	EBI1_D7	36 → EBI1_D6
CPU_PC17←	37	PC17	38 → CPU_PC12
CPU_PC25←	39	PC25	40 → CPU_PC20
CPU_PC27←		PC27	40 → CPU_PC28

X4			
GND←	1	PA20	2 → TWD
TWC←	3	PA21	4 → P_L1
P_L2←	5	LED1	6 → ERXM
ERXP←	7	RX+	8 → SPI0_MISO
ETP←	9	CT	10 → SPI0_MOSI
SPIO_NC←	11	PB3	12 → SPI1_SPCK
SPIO_SPCK←	13	PB3	14 → ETXM
ETXP←	15	PB2	16 → SPI1_MISO
ETXP←	17	TX+	18 → UART2_RX
UART1_RX←	19	PB5	20 → SPI1_NCS
SPI1_MOSK←	21	PB15	22 → ISL_D9
CPU_PD3←	23	PD3	24 → CPU_PB28
ISL_HS←	25	PB30	26 → ISL_D0
ISL_D6←	27	PB26	28 → ISL_MCK
ISL_D7←	29	PB27	30 → USB_HIP
ISL_D2←	31	PB22	32 → USB_HM
USB_HM←	33	USB1-	34 → C1_CMD
USB_HR←	35	USB0+	36 → C1_D0
C1_D1←	37	PA24	38 → C1_D2
C1_D3←	39	PA26	40 → UART1_TX
C1_CLK←		PA31	