SPECIFICATIONS

CUSTOMER

PTC

SAMPLE CODE (Ver.)

PS320240T-006-I-03 (Ver.0)

MASS PRODUCTION CODE (Ver.)

PH320240T-006-IP1Q (Ver.0)

DRAWING NO. (Ver.) PH-07020-004 (Ver.0)

Customer Approved

Date:

Approved	QC Confirmed	Designer
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Approval For Specifications C	Only. 7.07	· ·

* This specification is subject to change without notice.

Please contact Powertip or it's representative before designing your product based on this specification.

Approval For Specifications and Sample.

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History of Version

Date	Ver.	Description	Page	Design by
2007/8/3	0	MASS PRODUCTION.	ı	Jared

Total: 28 Page

Ver.0



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Packaging

Note: For detailed information please refer to IC data sheet:

Primacy(TFT LCD): Himax: HX8238-A



1. SPECIFICATIONS

1.1 Features

Main LCD panel

Item	Standard Value
Display Type	320(R、G、B) * 240 Dots
LCD Type	Normally white , Transmissive type
Screen size(inch)	3.5 inch
Viewing Direction	6 O'clock
Color configuration	RGB-Strip
Backlight	LED
Interface	Digital 24-bits RGB
Other(controller/driver IC)	Himax: HX8238-A
	THIS PRODUCT CONFORMS THE ROHS OF PTC
ROHS	Detail information please refer web side :
	http://www.powertip.com.tw/news/LatestNews.asp

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	76.9(W) * 63.9 (L) * 4.75 (H)(MAX)	mm

LCD panel

Item	Standard Value			
Viewing Area	72.88 (W) * 55.36 (L)	mm		
Active Area	70.08 (W) * 52.56 (L)	mm		

Touch panel

Item	Standard Value			
Viewing Area	72.0 (W) x 54.56 (H)	mm		
Active Area	70.08 (W) x 53.26 (L)	mm		

Note: For detailed information please refer to LCM drawing



1.3 Absolute Maximum Ratings

Module

Item	Symbol	Condition	Min.	Max.	Unit
System Power Supply Voltage	VDDIO	VSS=0	-0.3	4.0	V
Input Voltage	Vi	-	-0.3	5.0	V
Operating Temperature	T_OP	-	-20	70	°C
Storage Temperature	T _{ST}	-	-30	80	°C

1.4 DC Electrical Characteristics

Module VSS = 0V, Ta = $25^{\circ}C$

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Power Supply Voltage1	VDDIO	-	3.0	3.3	3.6	V
Vсом High Voltage	Vсомн	-	2.5	(3.6)	4.5	V
Vсом Low Voltage	Vcoml	-	-3	(-2.4)	0	V
Supply Current	IDD	VDD = 3.3 V Pattern= black *1	-	5.5	8.5	mA

Note1:Maximum current display



1.5 Optical Characteristics

TFT LCD Module

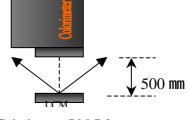
VDDIO=3.3V, Ta=25°C

Item		Symbol	Condition	Min.	Тур.	Max.	unit	-
Response tin	ne	Tr+ Tf	Ta = 25°C θX, θY = 0°	-	50	70	ms	Note2
	Тор	θΥ+		1	45	-		
Viewing angle	Bottom	θΥ-	CR ≥ 10	1	50	-	Deg.	Note4
viewing angle	Left	θΧ-	CIX 2 10	ı	50	-	Deg.	NOICH
	Right	θX+		-	50	-		
Contrast rati	0	CR	Ta = 25°C θX , θY = 0°	200	250	-	-	Note3
	\\/bito	X		0.244	0.294	0.344		
	White	Y		0.259	0.309	0.359	-	
	Red	Χ		0.577	0.627	0.677		
Color of CIE	rtcu	Υ		0.310	0.360	0.410		
(With B/L)	Coordinate (With B/L)	-	0.282	0.332	0.382	-		
	Green	Υ		0.506	0.556	0.606		
	Blue	X		0.091	0.141	0.191		NoteA
	Diue	Υ		0.040	0.090	0.140		
Average Brighti	ness							
Pattern=white display		IV	IF=20 mA	180	200	-	cd/m ²	
(With B/L)								
Uniformity (With B/L)*1		В	IF=20 mA	70	-	_	%	

Note A:

- *1: B=B(min) / B(max)
- *2 : Measurement Condition for Optical Characteristics:
 - a: Environment: 25 ±5 / 60±20%R.H, no wind, dark room below 10 Lux at typical lamp current and typical operating frequency.
 - b : Measurement Distance: $500 \pm 50 \text{ mm}$, $(\theta = 0^{\circ})$
 - c: Equipment: TOPCON BM-7 fast, (field 1°), after 10 minutes operation.
 - d: The uncertainty of the C.I.E coordinate measurement ± 0.01 , Average Brightness $\pm 4\%$





Colorimeter=BM-7 fast

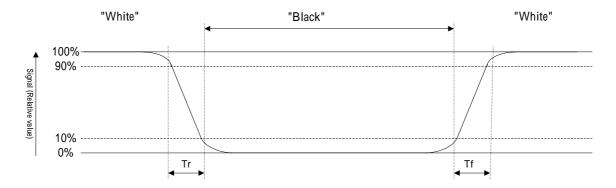


Note1: To be measured at the center area of panel with a viewing cone of 1° by Topcon luminance meter BM-7, after 10 minutes operation (module)

Note2: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

Refer to figure as below:



Note3: Definition of contrast ratio:

Contrast ratio is calculated with the following formula

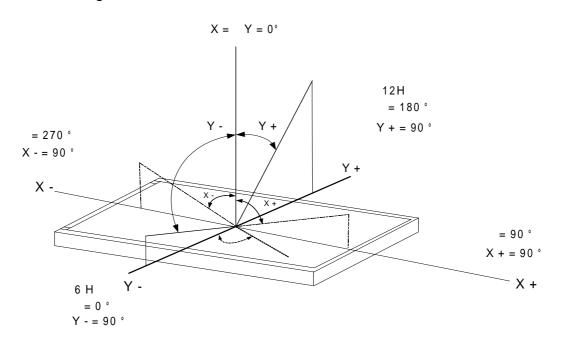
Photo detector output when LCD is at "White" state

Contrast ratio (CR) =

Photo detector output when LCD is at "Black" state

Note4: Definition of viewing angle:

Refer to figure as below:





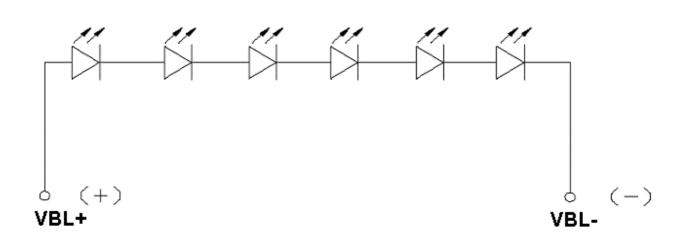
1.6 Backlight Characteristics

Maximum Ratings

	1	1			
Item	Symbol	Conditions	Min.	Max.	Unit
Forward Current	IF	Ta =25	-	30	mA
Reverse Voltage	VR	Ta =25	-	5	V
Power Dissipation	PD	Ta =25	-	0.720	W

Electrical / Optical Characteristics

Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Voltage	VF		18.0	19.8	21.0	V
Average Brightness (without LCD)	IV	IF= 20 mA	3000	3300	-	cd/m ²
CIE Color Coordinate	X		-	0.30	-	
(Without LCD)	Y		-	0.30	-	-
Color	White					





1.7 Touch Panel Characteristics

1	Input Method and Activation Force	Stylus < 80grams and Finger < 80grams				
2	Typical Optical	Visible Light Transmission : >80%				
	Characteristics	Haze: 10%(typ)				
		Operating Voltage 7V or less				
		2. Circuit close resistance X: 300~900 ohm				
2	Flactrical Charifications	Y : 200~800 ohm				
3	Electrical Specifications	3. Circuit open resistance > 10 Mohm at 25V DC				
		4. Contact bounce < 20ms				
		5. Operative resistance ≤ 2.0 kohm				
4	Linearity Tolerance	X≤1.5% (maximum), Y≤1.5% (maximum)				
		Operating Temperature -20°C ~ +70°C				
_		(Operating Humidity: 20%~90% RH)				
5	Environment Specification	Storage Temperature -30°C ~ +80°C				
		(Storage Humidity: 10%~90% RH)				



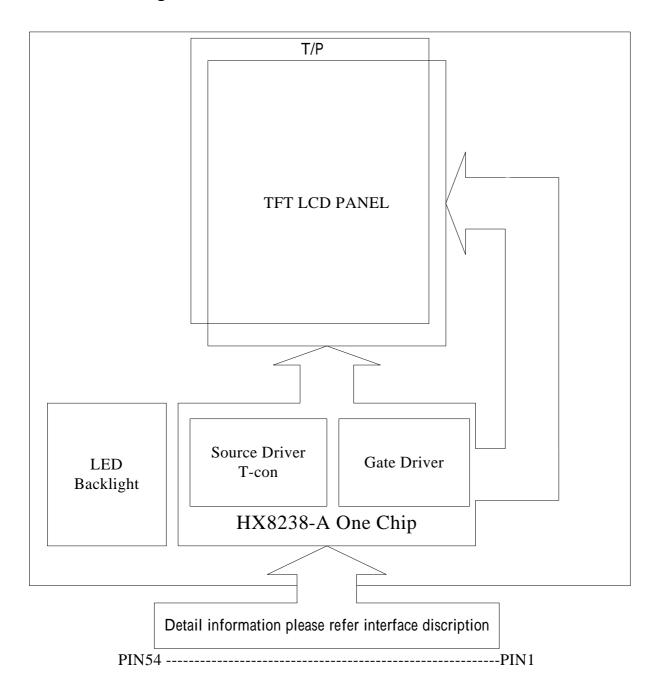
2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram





2.2 Interface Pin Description

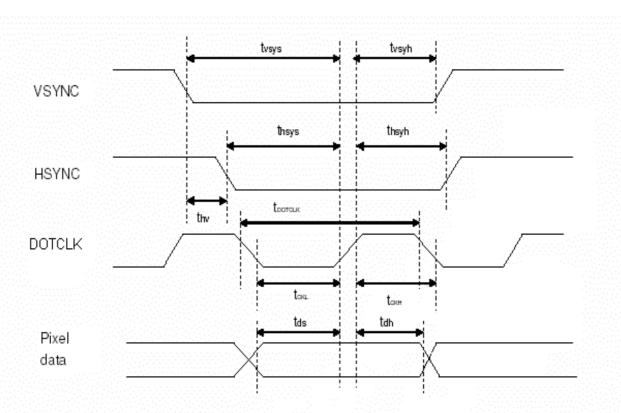
Pin No.	Symbol	Function
1	VBL-	Power supply for LED Backlight cathode input
2	VBL-	Power supply for LED Backlight cathode input
3	VBL+	Power supply for LED Backlight anode input
4	VBL+	Power supply for LED Backlight anode input
5	NC	Not used , Must be open
6	/RESET	Hardware reset
7	NC	Not used , Must be open. (Output Pin ,POL output.)
8	Y1	Touch panel TOP
9	X1	Touch panel RIGHT
10	Y2	Touch panel BOTTOM
11	X2	Touch panel LEFT
12	В0	Blue data bit 0
13	B1	Blue data bit 1
14	B2	Blue data bit 2
15	В3	Blue data bit 3
16	B4	Blue data bit 4
17	B5	Blue data bit 5
18	B6	Blue data bit 6
19	B7	Blue data bit 7
20	G0	Green data bit 0
21	G1	Green data bit 1
22	G2	Green data bit 2
23	G3	Green data bit 3
24	G4	Green data bit 4
25	G5	Green data bit 5
26	G6	Green data bit 6
27	G7	Green data bit 7
28	R0	Red data bit 0
29	R1	Red data bit 1
30	R2	Red data bit 2



31	R3	Red data bit 3
32	R4	Red data bit 4
33	R5	Red data bit 5
34	R6	Red data bit 6
35	R7	Red data bit 7
36	HSYNC	Horizontal sync input
37	VSYNC	Vertical sync input
38	DOTCLK	Dot data clock
39	VDDIO	Digital power
40	VDDIO	Digital power
41	VDDIO	Digital power
42	VDDIO	Digital power
43	SPENA	Serial port data enable signal
44	NC	Not used , Must be open
45	NC	Not used , Must be open (Output Pin ,V _{GL} ,Gate off power.)
46	NC	Not used , Must be open
47	NC	Not used , Must be open (Output Pin ,V _{GH} , Gate on power.)
48	SHUT	Display shut down pin to put the driver into sleep mode. A sharp falling edge must be provided to such pin when IC power on. Internal pull low Connect to VDDIO for sleep mode - Connect to VSS for normal operating mode (Refer to Power Up Sequence)
49	SPCLK	Serial data clock
50	SPDAT	Serial data
51	NC	Not used , Must be open (Output Pin ,VCOM power.)
52	ENB	Data enable control
53	VSS	Ground
54	VSS	Ground



2.3 Timing Characteristics



Pixel timing

Characteristics	Cumbal	Sumbal Mit		Ty	Тур		Max	
Characteristics	Symbol	24 bit	8 bit	24 bit	8 bit	24 bit	8 bit	Unit
DOTCLK Frequency	fDOTCLK	.,		6.5	19.5	10	30	., . MHz
DOTCLK Period	tDOTCLK	100	33.3	154	51.3	, =		ns
Vertical Sync Setup Time	tvsys	20 .	10					ทร
Vertical Sync Hold Time	tvsyh	20	. 10	,		,		ns
Horizontal Sync Setup Time	thsys	20	.10					ns
Horizontal Sync Hold Time	thsyh	20	. 10					ns
Phase difference of Sync Signal Falling Edge	thv					24	10	tDOTCLK
DOTCLK Low Period	tCKL	50	15					ns
DOTCLK High Period	tCKH	50	15	-		-		ns
Data Setup Time	tds	12	10	,		,		ns
Data hold Time	tdh	12	10					ns . _.
Reset pulse width	tRES	1	0					us

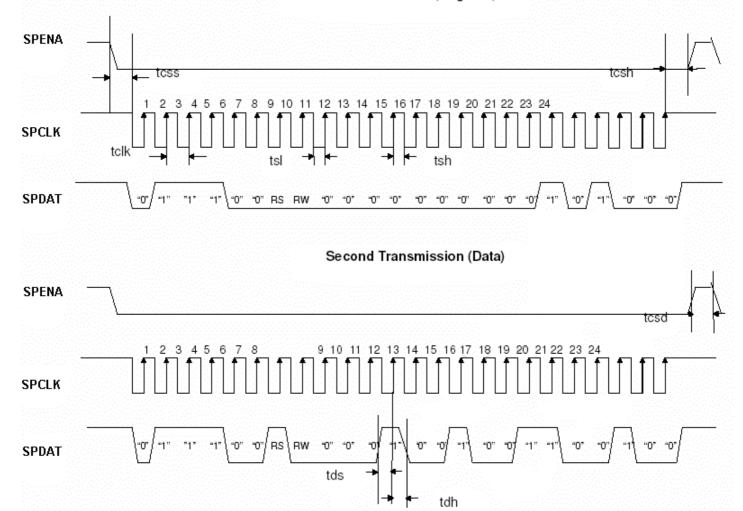
Note: External clock source must be provided to DOTCLK pin of HX8238-A. The driver will not operate if absent of the clocking signal.

Pixel timing

Note: The interface of this module can drive by digital 24-bit data.



First Transmission (Register)



Note: The example transmit "0x1264h" to register R28h. SPID connected to VSS.

SPI interface timing diagram & transaction example

Characteristics	Symbol	Min	Тур	Max	Unit
Serial Clock Frequency	fclk	2000 - 2000		20	MHz
Serial Clock Cycle Time	tclk	50	.	-	ns
Clock Low Width	tsl	25		-	ns
Clock High Width	tsh	25			ns
Chip Select Setup Time	tcss	0	.	::::::::::::::::::::::::::::::::::::::	ns
Chip Select Hold Time	tcsh	10	- ,		ns
Chip Select High Delay Time	tcsd	20	-	-	ns
Data Setup Time	tds	5	.	.	ns
Data Hold Time	tdh	10	-		ns

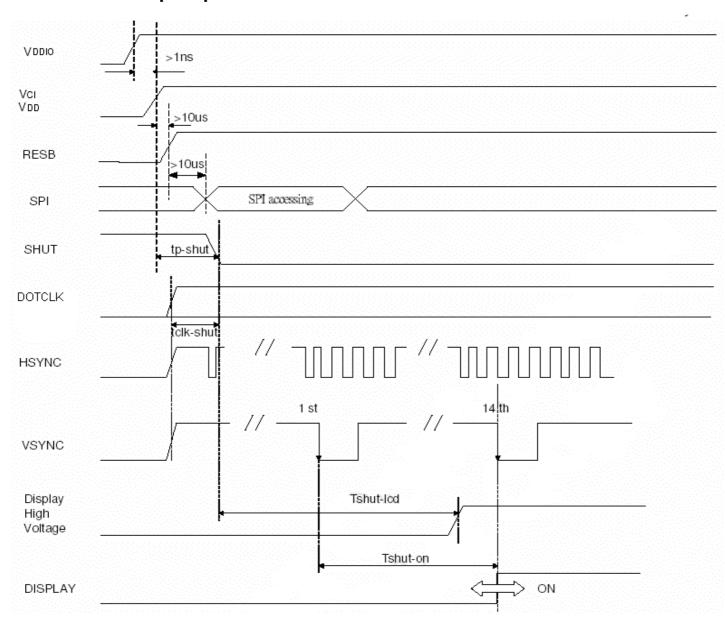
SPI timing

Ver.0



2.4 Power Sequence

2.4.1 Power up sequence



Characteristics	Symbol	Min	Тур	Max	Units
VDDD / VDDIO on to falling edge of SHUT	tp-shut	1			us
DOTCLK	tclk-shut	:::::1::::::::	-		clk
Falling edge of SHUT to LCD power on	tshut-lcd	.	.	128	ms
Falling edge of SHUT to display start		.	.	14	frame
- 1 line: 408 clk - 1 frame: 262 line -DOTCLK = 6.5MHz	tshut-on		166	232.4	ms

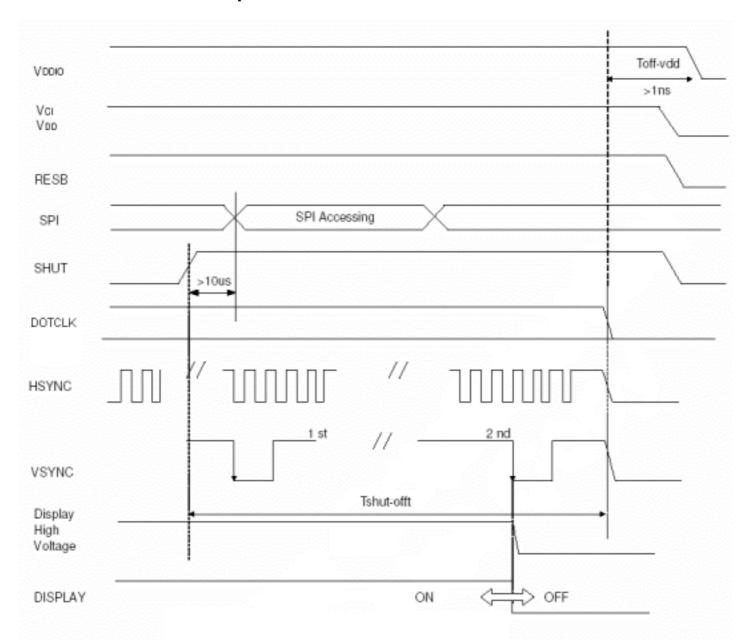
Note: It is necessary to input DOTCLK before the falling edge of SHUT.

Display starts at 10th falling edge of VSTNC after the falling edge of SHUT.

Note: 1. The voltage of VDD be boosted from VDDIO.



2.4.2 Power down sequence



Characteristics	Symbol	Min	Тур	Max	Uni
Rising edge of SHUT to display off	A statistical stat	2		a transfer to the first of the	frame
- 1 line: 408 clk - 1 frame: 262 line - DOTCLK = 6.5MHz	tshut-off	33.4		-	ms
Input-signal-off to VDDD / VDDIO off	toff-vdd	1		against - market a	us

Note: DOTCLK must be maintained at lease 2 frames after the rising edge of SHUT.

Display become off at the 2nd falling edge of VSTNC after the falling edge of SHUT.

If RESET signal is necessary for power down, provide it after the 2-frames-cycle of the SHUT period.

Note: 1、The voltage of VDD be boosted from VDDIO.



2.5 Reference Initial code

Register(0x0001);
Data(0x7300);

Register(0x0002);
Data(0x0200);

Register(0x0003);
Data(0x6364);

Register(0x0004);
Data(0x04C7);

Register(0x0005);
Data(0xFC80);

Register(0x00,0x0A); //Cont

Register(0x00,0x0A); //Contrast/Brightness control; Data(0x4008);

Register(0x00,0x0D); //Po

Data(0x3229);

//Power control(2);

Register(0x00,0x0E); //Power control(3);VOML

Data(0x3200);

Register(0x00,0x1E);

Data(0x00D2);

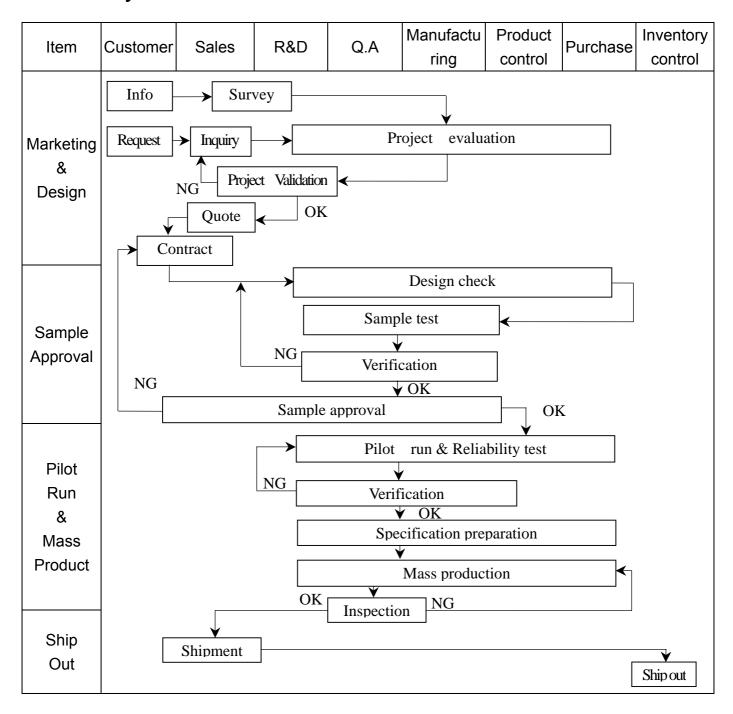
//Power control(4);COMH

Ver.0

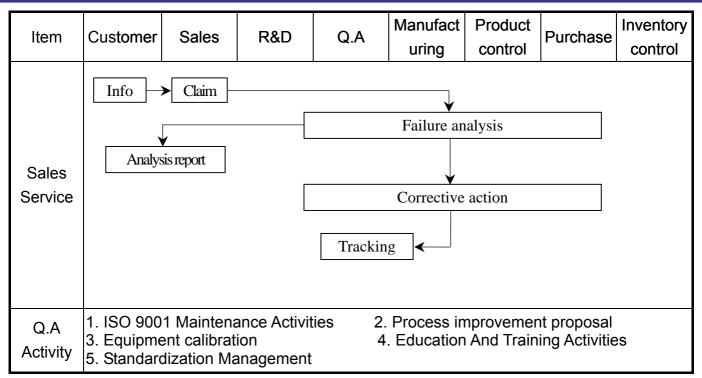


3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart









3.2 Inspection Specification

1. Inspection Specification

◆Scope : The document shall be applied to TFT-LCD Module for 3, 5" ~10" (Ver. 02).

◆Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.

◆Equipment: Gauge > MIL-STD > Powertip Tester > Sample

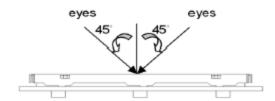
◆Defect Level: Major Defect AQL: 0.4; Minor Defect AQL: 1.5

♦OUT Going Defect Level: Sampling.

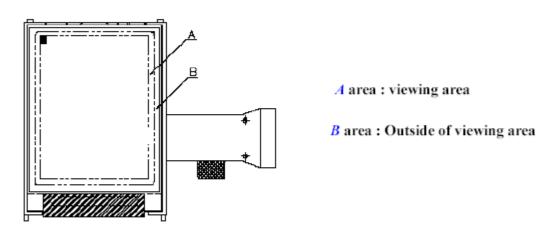
Standard of the product appearance test:

a. Manner of appearance test:

- (1). The test best be under 20W×2 fluorescent light, and distance of view must be at 30 cm.
- (2). The test direction is base on about around 45° of vertical line.



(3). Definition of area.



(4). Standard of inspection: (Unit: mm)



NO	Item		Criterion				Level	
		ı	1. 1The part number is inconsistent with work order of production.					
01	Product condition	1. 2 Mix	. 2 Mixed product types.					
		1. 3 Ass	. 3 Assembled in inverse direction.					
02	Quantity	2, 1The	quantity	y is inconsistent with	work order of product	ion.	Major	
03	Outline dimension		duct dir gram.	mension and structi	ire must conform to st	ructure	Major	
		4. 1 Mis	4. 1 Missing line character and icon.					
		4, 2 No	4, 2 No function or no display.					
04	Electrical Testing	lectrical Testing 4, 3 Display malfunction.					Major	
		4. 4 LCD viewing angle defect.					Major	
		4. 5 Cu	4. 5 Current consumption exceeds product specifications.				Major	
				Item	Acceptance (Q'ty)			
	Dot defect			Bright Dot	≦ 4			
	Dot delect		Dot	Dark Dot	≦ 5			
	(Bright dot \		Defect	Joint Dot	≦ 3			
05	Dark dot)			Total	≦ 7		Minor	
	On -display	5. 1 Ins	5. 1 Inspection pattern: full white, full black, Red, Green and blue screens.					
		5. 2 It is	s defined	as dot defect if defe	ct area >1/2 dot.			
		5. 3 The	e distanc	e between two dot d	efect ≧5 mm.			



NO	Item		Criterion				
NO	Black or white dot > scratch > contamination Round type			r display) :	eptance (Q'ty) Ignore 5 0		Level
06	$\Phi = (x+y)/2$ Line type $L \mapsto L$	6. 2 Line type(No. 2	0.03 < W	(W) $Y \le 0.03$ $Y \le 0.05$	Acceptance (Q'ty) Ignore 4 2 As round type 5		Minor
07	Polarizer Bubble	0. 25 < 0. 50 <	diameter : Φ) $\Phi \leq 0.25$ $\Phi \leq 0.50$ $\Phi \leq 0.80$ $\Phi > 0.80$ otal	Acc	eptance (Q'ty) Ignore 4 1 0 5		Minor



NO	Item		Criterion		Level
NO Item OB The crack of glas		t : The thick	th of crack kness of crack kness of glass	k between panels:	Level
		X ≤ a	eal width Y Crack can't enter	Z ≤1/2 t	
		\parallel	viewing area Crack can't exceed the	1/2 t < Z ≤2 t	



(Ver. 02)

NO	Item	Criterion	Level
		Symbols: X: The length of crack Z: The thickness of crack t: The thickness of glass 8.1.2 Corner crack:	
		X Y Z	
		$\leq 1/5$ a Crack can't enter viewing area $Z \leq 1/2$ t	
		$\leq 1/5$ a Crack can't exceed the half of SP width. $1/2$ t $<$ Z ≤ 2 t	
08	The crack of glass	e a Bestevation construction I	Minor
		8.2 Protrusion over terminal : 8.2.1 Chip on electrode pad :	
		$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	

Ver.0



NO	Item	Criterion	Level
NO	Item	Symbols: X: The length of crack Z: The thickness of crack t: The thickness of glass 8.2.2 Non-conductive portion:	Level
08	The crack of glass	X Y Z ≤ 1/3 a ≤ W ≤t O If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications. 8. 2. 3 Glass remain:	Minor
		$\begin{array}{c cccc} X & Y & Z \\ & \leq a & \leq 1/3 \text{ W} & \leq t \end{array}$	



NO	Item	Criterion	Level
		9, 1 Backlight can't work normally.	Major
09	Backlight elements	9, 2 Backlight doesn't light or color is wrong.	Major
		9. 3 Illumination source flickers when lit.	Major
	General appearance	10. 1 Pin type \quantity \	Major
		10, 2 No short circuits in components on PCB or FPC .	Major
		10. 3 Parts on PCB or FPC must be the same as on the production characteristic chart .There should be no wrong parts , missing parts or excess parts.	Major
10		10. 4 Product packaging must the same as specified on packaging specification sheet.	Minor
		10. 5 The folding and peeled off in polarizer are not acceptable.	Minor
		$10,6$ The PCB or FPC between B/L assembled distance(PCB or FPC) is $~\leq 1,5$ mm.	Minor



4. RELIABILITY TEST

4.1 Reliability Test Condition

Ver.02

NO		EM TEGE CONDITION			
NO.	TEST ITEM	TEST CONDITION			
1	High Temperature Storage Test	Keep in +80 ±2 96 hrs Surrounding temperature, then storage at normal condition 4hrs.			
2	Low Temperature Storage Test	Keep in -30 ±2 96 hrs Surrounding temperature, then storage at normal condition 4hrs.			
3	High Temperature / High Humidity Storage Test	Keep in +60 / 90% R.H duration for 96 hrs Surrounding temperature, then storage at normal condition 4hrs. (Excluding the polarizer)			
4	ESD Test	Air Discharge: Apply 2 KV with 5 times Discharge for each polarity +/- 1. Temperature ambiance: 15 2. Humidity relative: 30% 60% 3. Energy Storage Capacitance(Cs+Cd): 150pF±10% 4. Discharge Resistance(Rd): 330 ±10% 5. Discharge, mode of operation: Single Discharge (time between successive discharges at least 1 sec) (Tolerance if the output voltage indication: ±5%)			
5	Temperature Cycling Storage Test	-20 +25 +70 +25 (30mins) (5mins) (30mins) (5mins) 10 Cycle Surrounding temperature, then storage at normal condition 4hrs.			
6	Vibration Test (Packaged)	 Sine wave 10 55 Hz frequency (1 min) The amplitude of vibration :1.5 mm Each direction (X, Y, Z) duration for 2 Hrs 			
7	Drop Test (Packaged)	Packing Weight (Kg) 0 ~ 45.4 45.4 ~ 90.8 90.8 ~ 454 Over 454 Drop direction: 1 corner / 3 edge	Drop Height (cm) 122 76 61 46 es / 6 sides each 1 times		



5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is 320±10 and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM.

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is 25 ±5 and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

5.4 TERMS OF WARRANTY

5.4.1 Applicable warrant period

The period is within thirteen months since the date of shipping out under normal using and storage conditions.

5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment , we cannot take responsibility if the product is used in nuclear power control equipment , aerospace equipment , fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.

LCM Model 版次Ver.0 Pl

PH320240T-006-IP1Q

LCM包裝規格書 LCM Packaging Specifications (For Tray)

1. 包裝材料規格表 (Packaging Material): (per carton)

No.	Item	Model	Dimensions (mm)	Quantity
1	成品 (LCM)	PH320240T-006-IP1Q	76.9 X 63.9	252
2	多層薄膜(1)POF	OTFILM0BA03ABA	19"X350X0.015	6
3	TRAY盤(2)Tray	TYPH32024004BA	352 X 260 X 12.8	48
4	内盒(3)Product Box	BX36627063ABBA	393 X 274 X 68	6
5	保力龍板(4)Polylon board	OTPLB00PL08ABA	550 X 393 X 20	2
6	外紙箱(5)Carton	BX57041027CCBA	570 X 410 X 265	1
7				
8				
9				

2. 單箱數量規格表 (Packaging Specifications and Quantity):

(1)LCM quantity per box : no per tray

6

x no of tray

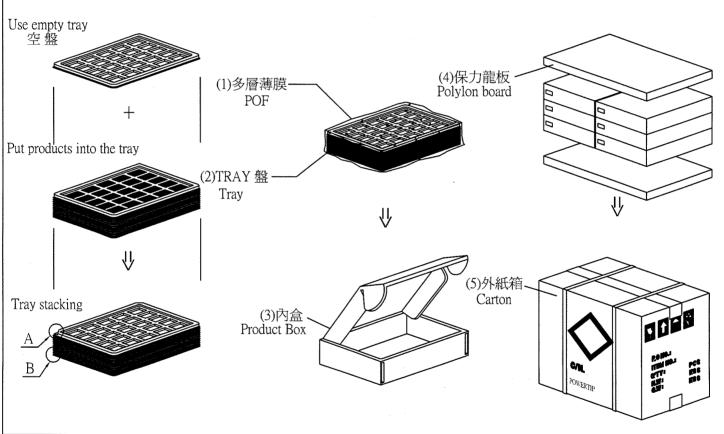
7 = 42

(2) Total LCM quantity in carton: quantity per box

42

x no of boxes

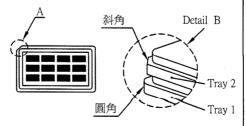
6 = 252



特記事項(REMARK)

1. Label Specifications:

MODEL:
LOT NO:
QUANTITY:
CHECK:



2.TRAY盤相疊時,需旋轉180度,請詳見B視圖 Rotate tray 180 degrees and place on top of stack. Check the tray stack using Fig. B.

- 3.可適用於單品包裝 It's also suitable to Panel
- 4.Tray料號:

Tray Number:PT-PH320240T-004-1

